

AT91 ARM Overview



AT91 ARM Series	Device	Family, Part	Core	MMU/-I-D Cache	MHz (@5°C)*	SRAM Bytes (x32)	FLASH Bytes (x16/32)	BOOT ROM Bytes	External Businterface	SDRAM IF	LIN/J1587 USART	USART/D08BU	Enhanced USART	SPI	TWI	SSC	PDC Channels	MCI	CAN	USB Device Full Speed	USB Host Full Speed	Ethernet MAC10/100	AES & Triple-DES	Camera Interface
SAM7S General purpose ARM7TDMI, low cost, single chip Flash MCUs with USB	AT91SAM7S32-AU-001	ARM7TDMI	55	8K	32K (x32)	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
	AT91SAM7S21-AU-001	ARM7TDMI	55	8K	32K (x32)	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
	AT91SAM7S64-AU-001	ARM7TDMI	55	16K	64K (x32)	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
	AT91SAM7S128-AU-001	ARM7TDMI	55	32K	128K (x32)	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
	AT91SAM7S256-AU-001	ARM7TDMI	55	64K	256K (x32)	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
	AT91SAM7S512-AU-001	ARM7TDMI	55	128K	512K (x32)	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
	AT91SAM7SE12-AU	ARM7TDMI	MPU	55	32K	512K (x32)	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
	AT91SAM7SE256-AU	ARM7TDMI	MPU	55	32K	256K (x32)	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
	SAM7SE General purpose ARM7TDMI Flash MCUs with USB and 32-bit SDRAM EBI																							
	SAM7X General purpose ARM7TDMI, single chip Flash MCUs with USB, CAN and Ethernet	AT91SAM7X128-AU	ARM7TDMI	55	32K	128K (x32)	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
AT91SAM7X256-AU		ARM7TDMI	55	64K	256K (x32)	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
SAM7XC General purpose ARM7TDMI, single chip Flash MCUs with USB, CAN, Ethernet and Crypto Hardware	AT91SAM7XC128-AU	ARM7TDMI	55	32K	128K (x32)	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
	AT91SAM7XC256-AU	ARM7TDMI	55	64K	256K (x32)	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
SAM7A Multiple CAN ARM7TDMI, LIN USARTs with EBI or single chip Flash MCU	AT91SAM7A1-AU	ARM7TDMI	40	4K	-	-	-	3	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
	AT91SAM7A2-AU	ARM7TDMI	30	16K	-	-	2	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
	AT91SAM7A3-AU	ARM7TDMI	60	32K	256K (x32)	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
	AT91SAM7A7A	ARM7TDMI	40	4K	-	-	3	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
	AT91SAM7A7B	ARM7TDMI	30	16K	-	-	2	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
	AT91SAM7A7C	ARM7TDMI	60	32K	256K (x32)	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
AT91 ARM7	AT91M40800-33AU	ARM7TDMI	40	8K	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
	AT91R40008-66AU	ARM7TDMI	70	256K	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
	AT91FR40162S	ARM7TDMI	70	256K	2M (x16)	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
	AT91M42800A-33AU	ARM7TDMI	33	8K	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
	AT91M55800A-33AU	ARM7TDMI	33	8K	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
	AT91RM3400-AU	ARM7TDMI	66	96K	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
AT91 ARM9	AT91RM9200-CJ-002	ARM920T	180	16K	MMU/2x16K	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
	AT91SAM9260-CJ	ARM926J-S	180	2x4K	MMU/2x8K	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
	AT91SAM9260-QU	ARM926J-S	180	2x4K	MMU/2x8K	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
	AT91SAM9261-CJ	ARM926J-S	180	160K	MMU/2x16K	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
	AT91RM9200-QU-002	ARM920T	180	16K	MMU/2x16K	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
	AT91RM9200-QU-002	ARM920T	180	16K	MMU/2x16K	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-

ARM920T: ARM9TDMI RISC CPU, 16KB Instruction and 16KB Data caches, MMU, Write buffer and Embedded Trace Macrocell.
 ARM926EJ-S: ARM9EJ-S CPU, 16KB Instruction and 16KB Data caches, MMU, Write buffer and Embedded Trace Macrocell.
 ARM926EJ-S: ARM9EJ-S CPU, 16KB Instruction and 16KB Data caches, MMU, Write buffer, DSP Instruction extension, Java acceleration and Embedded Trace Macrocell.

Enhanced USART: support RS485, ISO7816, IrDA and modem control lines with PDCs
 Debug UART provides single entry to all debug capabilities as well as 2 wire UART function with PDCs

DBGU: support RS485, ISO7816, IrDA and modem control lines with PDCs

